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O.I.P.E. PATENT DATE SCANNED

ADDITION						<u>.</u>
APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	Tevanies	
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Jerry D. Kline

Method for selecting components for a matched set from a wafer-interposer assembly

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ORIGINAL	CHOSS REFERENCE(S)				
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TERMINAL	DRAWINGS	CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg. Figs. Drwg. Print Fig.	Total Claims Print Claim for O.G.		
The term of this patent subsequent to (date) has been discislined.	Seria Mar La 45953 Sesistant Examine)	NOTICE OF ALLOWANCE MAILED		
The term of this patent shall not extend beyond the expiration date.				
of U.S Patent. No.		ISSUE FEE		
	(Primary Examiner) (Date)	Amount Due Date Paid		
The terminalmonths of this patent have been disclaimed.		ISSUE BATCH NUMBER		
WARNING: The Information disclosed herein may be res	ticled. Unauthorized disclosure may be prohibited by the Lark Office is restricted to authorized employees and contract	Inited States Code Title 35, Sections 122, 181 and 38		